



#33

Sheet 1 of 1

Form 1449*	Atty. Docket No.: 303.378US1	Serial No. 08/903,453
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Leonard Forbes et al.	
	Filing Date: July 29, 1997	Group: 2815

U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<i>JSC</i>	5,698,869	12/16/1997	Yoshimi, M., et al.	257	192	09/13/95
<i>JSC</i>	5,754,477	05/19/1998	Forbes, L.	365	185.33	01/29/97
<i>JSC</i>	5,886,376	03/23/1999	Acovic, A., et al.	257	316	07/01/96
<i>JSC</i>	5,976,926	11/02/1999	Wu, J.Z., et al.	438	237	10/10/97
<i>JSC</i>	6,084,248	07/04/2000	Inoue, S.	257	66	02/03/98
<i>JSC</i>	6,093,937	07/25/2000	Yamazaki, S., et al.	257	59	02/19/97
<i>JSC</i>	6,099,574	08/08/2000	Fukuda, S., et al.	703	14	12/16/97
<i>JSC</i>	6,166,401	12/26/2000	Forbes, L.	257	77	08/20/98
<i>JSC</i>	6,271,566	08/07/2001	Tsuchiaki, M.	257	347	11/16/99

FOREIGN PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
-----------------------	-----------------	------	---------	-------	----------	-------------------------

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

<i>JSC</i>	Wolf, "Silicon Processing for the VLSI Era Vol. 2: Process Integration", <u>Lattice Press</u> , 623-626, (1990)
------------	--

BEST AVAILABLE COPY

RECEIVED
FEB 15 2002
1C 2800 MAIL ROOM

Examiner <i>JSC Eshwari</i>	Date Considered <i>6/20/02</i>
-----------------------------	--------------------------------

*Substitute Disclosure Statement Form (PTO-1449)

**EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.